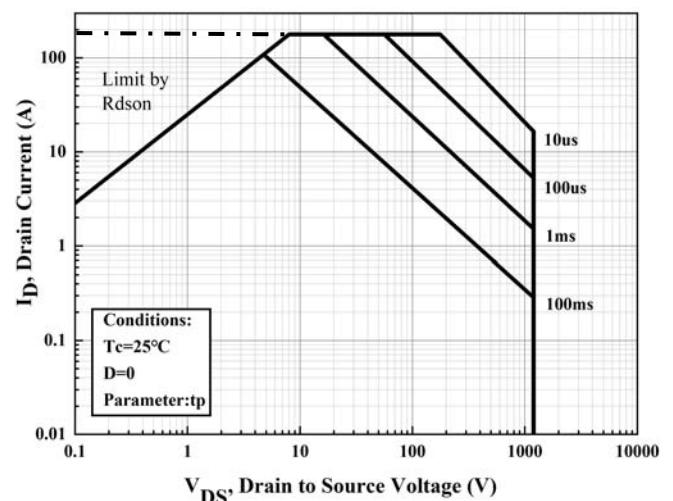

 Figure 21. Switching Times vs. R_g


Figure 22. Safe Operating Area

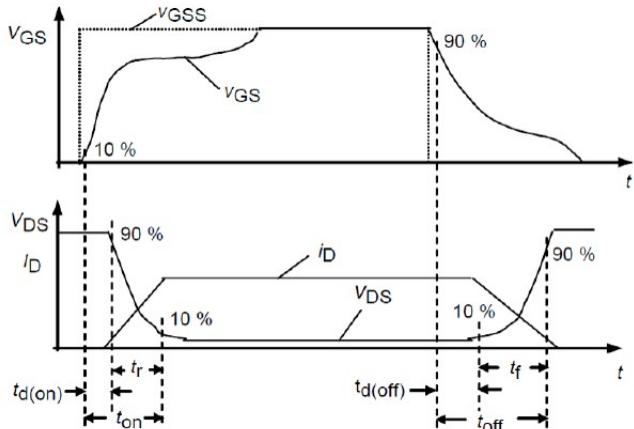


Figure 23. Switching Times Definition

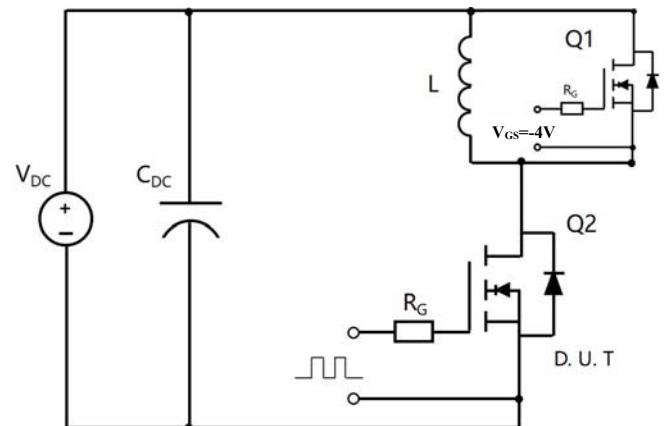


Figure 24. Clamped Inductive Switching Waveform Test Circuit



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